When Configuring MVA are there alignment requirements for the buffer size?

Model:

ARC Series C Board (10G-PCIE2-8C2-2S) and ARC Series B Board (10G-PCIE2-8B-2S)

Software Release:

MVA Software Release 1.3.0

Operating System:

Linux, Centos 8.x and RHEL 8.x

Information:

Question: When configuring MVA are there alignment requirements for the buffer size?

Answer: No, we do not have any alignment requirements for the buffer size.

Revision	Date	Change
1	3/5/2021	Initial Draft